

IN THE SPECIFICATION

Please amend the third paragraph on page one (1) as follows:

The reason for the imbalance is that implantation of transistors is carried out on only one of the two surfaces of the substrates. To minimize the perception of flickering in an actively driven LCD, row, column, and pixel inversions have been proposed. The flickering effect is thus spatially averaged out to an extent that is imperceptible.

However, this results in reduced contrast due to a fringe field effect occurring on the pixel boundary. The loss is usually negligible when the pixel size is not too small.

However, in the case of a miniature display (e.g. amorphous-silicon TFT, poly-silicon TFT, and miniature liquid crystal on silicon), the loss in contrast can be ~~severe~~ and cannot be compensated.

Please amend the second paragraph on page seven (7) as follows:

In each case there is a switching signal 5015, the ITO voltage being shown at 16.